

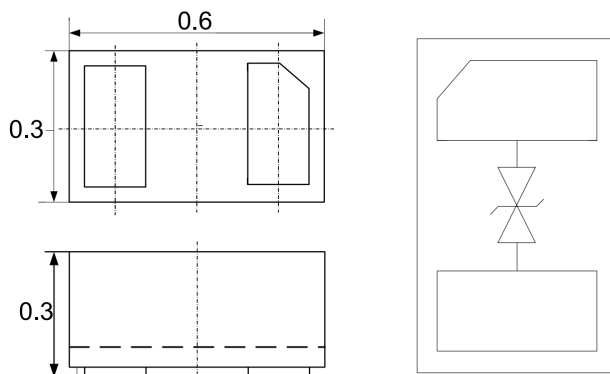
Description

The UL2421P0 is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The UL2421P0 has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) standard with $\pm 15\text{kV}$ air and $\pm 8\text{kV}$ contact discharge. It is assembled into an ultra-small 0.6x0.3x0.3mm lead-free DFN package. The small size, ultra-low capacitance and high ESD surge protection make UL2421P0 an ideal choice to protect cell phone, digital visual interfaces, HDMI, DVI, USB 2.0, USB 3.0, and other high speed ports.

Mechanical Characteristics

- ◆ Package: DFN0603-2 (0.6 x0.3 x0.3mm)
- ◆ Case Material: "Green" Molding Compound.
- ◆ Moisture Sensitivity: Level 3 per J-STD-020
- ◆ Terminal Connections: See Diagram Below
- ◆ Marking Information: See Below

Dimensions and Pin Configuration



Package Dimensions (mm) Circuit and Pin Schematic

Features

- ◆ Ultra small package: 0.6 x0.3 x0.3mm
- ◆ Ultra low capacitance: 0.3pF typical
- ◆ Ultra low leakage: nA level
- ◆ Operating voltage: 24V
- ◆ Low clamping voltage
- ◆ 2-pin leadless package
- ◆ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
Air discharge: $\pm 15\text{kV}$
Contact discharge: $\pm 8\text{kV}$
 - IEC61000-4-5 (Lightning) 1.5A (8/20 μs)
- ◆ RoHS Compliant

Applications

- ◆ Cellular Handsets and Accessories
- ◆ Display Ports
- ◆ MDDI Ports
- ◆ USB Ports
- ◆ Digital Video Interface(DVI)
- ◆ PCI Express and Serial SATA Ports

Marking Information



24R = Device Marking Code

Ordering Information

Part Number	Marking	Packaging	Reel Size
UL2421P0	24R	10000/Tape & Reel	7 inch

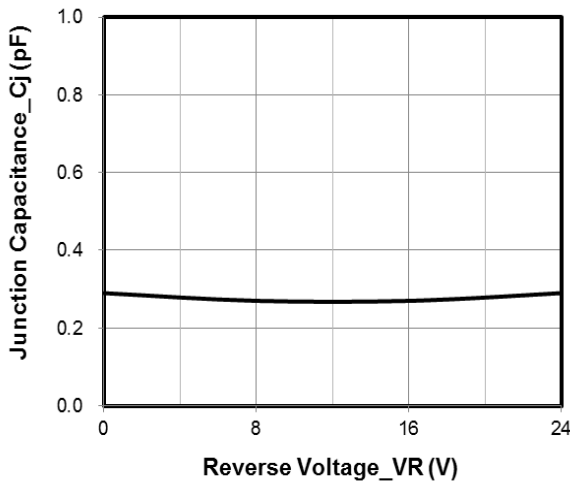
Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power(8/20 μs)	Ppk	80	W
Peak Pulse Current(8/20 μs)	Ipp	1.5	A
ESD per IEC 61000-4-2 (Air)	VESD	± 15	kV
ESD per IEC 61000-4-2 (Contact)		± 8	
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +125	$^{\circ}\text{C}$

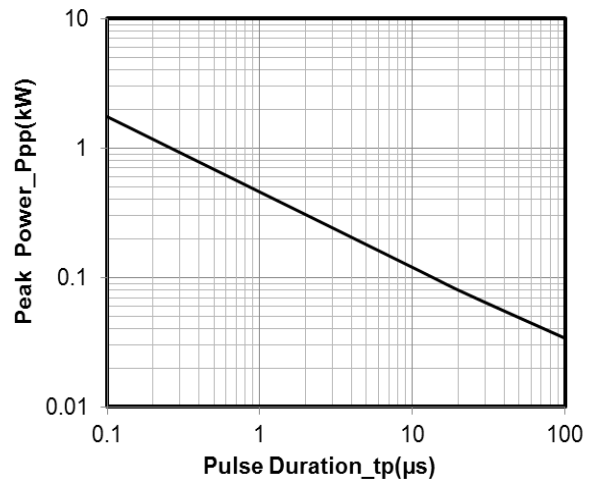
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			24	V	
Breakdown Voltage	VBR	26.5			V	IT = 1mA
Reverse Leakage Current	IR			0.5	μA	VRWM = 24V
Clamping Voltage	VC			40	V	I _{PP} = 1A (8 x 20 μs pulse)
Clamping Voltage	VC			53	V	I _{PP} = 1.5A (8 x 20 μs pulse)
Junction Capacitance	CJ		0.3		pF	VR = 0V, f = 1MHz

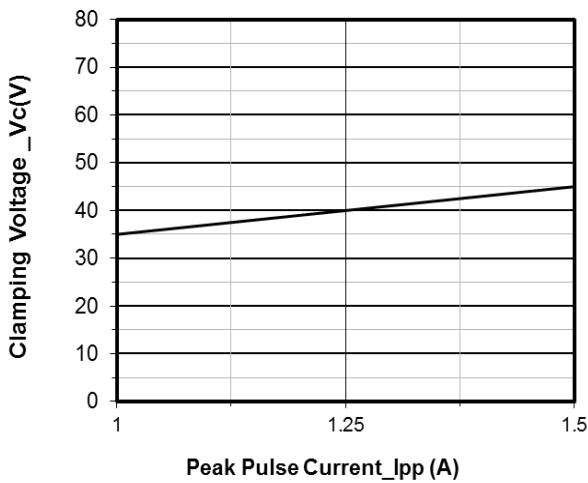
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



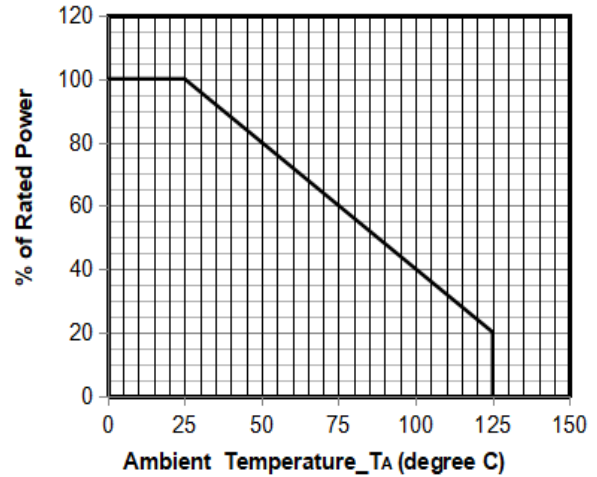
Junction Capacitance vs. Reverse Voltage



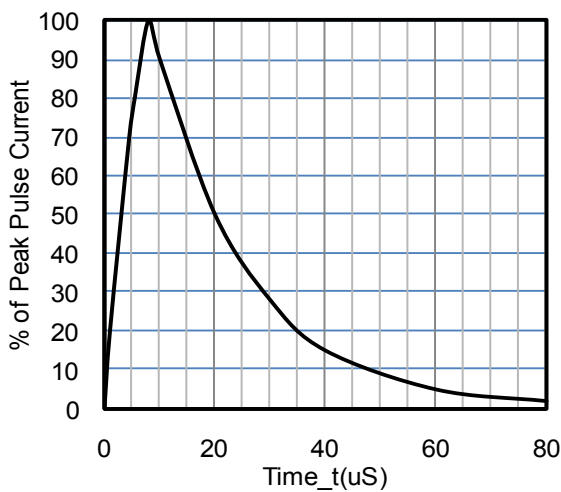
Peak Pulse Power vs. Pulse Time



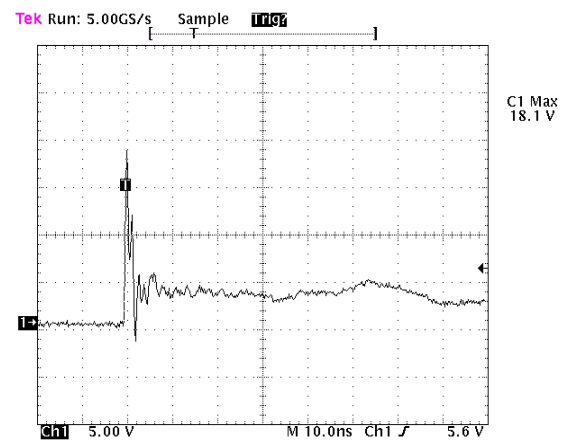
Clamping Voltage vs. Peak Pulse Current



Power Derating Curve



8 X 20µs Pulse Waveform

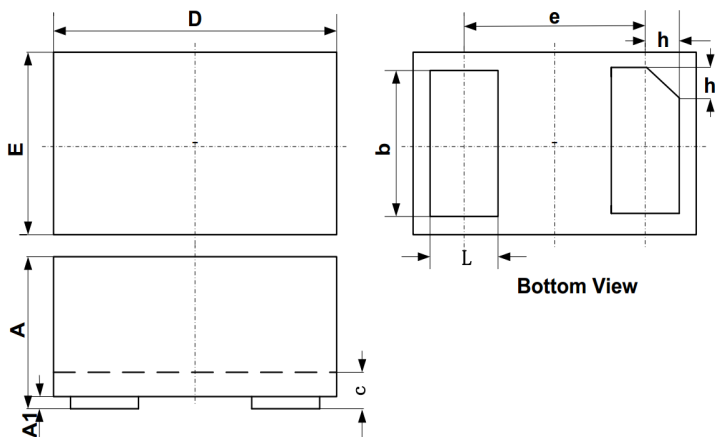


Note: Data is taken with a 10x attenuator

ESD Clamping Voltage

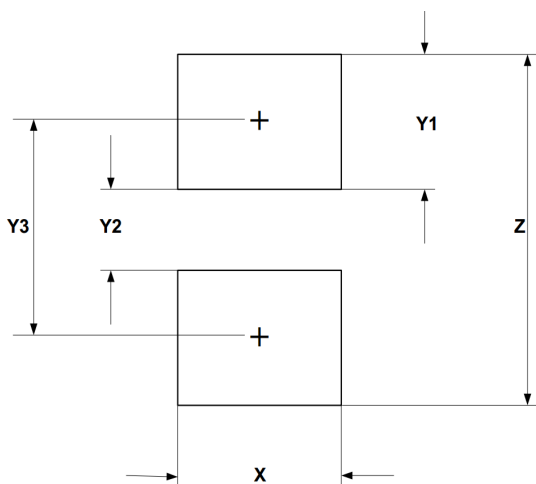
8 kV Contact per IEC61000-4-2

DFN0603-2 Package Outline Drawing



SYM	DIMENSIONS		
	MILLIMETERS		
	MIN	NOM	MAX
A	0.230		0.330
A1	0.000	0.020	0.050
b	0.215	0.245	0.275
c	0.120	0.150	0.180
D	0.550	0.600	0.650
e	0.355 BSC		
E	0.250	0.300	0.350
L	0.160	0.190	0.220
h	0.079 BSC		

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	0.30	0.012
Y1	0.25	0.010
Y2	0.15	0.006
Y3	0.40	0.016
Z	0.65	0.026